DEPARTMENT OF ELECTRICAL ENGINEERING
राष्ट्रीय प्रौद्योगिकी संस्थान श्रीनगर
NATIONAL INSTITUTE OF TECHNOLOGY SRINAGAR
(An autonomous Institute of National Importance under the aegis of Ministry of Education, Govt. of India)
हजरतबल, श्रीनगर, जम्मू-कश्मीर, 190006, भारत
Hazratbal, Srinagar Jammu and Kashmir, 190006, INDIA

NOTICE
Dated: - 20/11/2021

The date, timing and venue of Written Test for admission to a PhD programme in the Electrical Engineering Department for Autumn 2021 is **23rd of November, 2021, 11.00 A.M. in Hi-Tech-5.**
Candidate List:-

S. No	Name	Roll No.
1.	SEHRISH MANZOOR	101
2.	TANIYA MANZOOR	102
3.	MUZAFFAR AHMAD DAR	104
4.	NASIR UL ISLAM WANI	105
5.	HASHIM RASHID	106
6.	SWATI SABNAM GAN	107
7.	RUQAYA MOHI-U-DIN	108
8.	MUNAZAMA ALI	109
9.	SAIKA ANJUM	110
10.	BATOOL ROUF	111
11.	WAJID MAJEED	112
12.	ABDUL HAMID	113
13.	SAJJAD BADAR	114
14.	ADNAN MUKHTAR	115
15.	FARISHTA	116
16.	OVAIS NAZIR	117

The candidates are requested to remain in touch with the institute website for any further updates in this regard.

Dr. Neeraj Gupta
Ph.D. Coordinator
EED
NIT Srinagar.