विद्युत अभियांत्रिकी विभाग / DEPARTMENT OF ELECTRICAL ENGINEERING राष्ट्रीय प्रौद्योगिकी संस्थान श्रीनगर / NATIONAL INSTITUTE OF TECHNOLOGY SRINAGAR (शिक्षा मंत्रालय, भारत सरकार के तत्वावधान में राष्ट्रीय महत्व का एक संस्थान) (An Institute of National Importance under the aegis of Ministry of Education, Govt. of India) हजरतबल, श्रीनगर, जम्मू-कश्मीर, १९०००६, भारत Hazratbal, Srinagar Jammu and Kashmir, 190006, INDIA



Date: 30/08/2023

Advertisement for the post of Junior Research Fellow under SAP 2022 Project

Applications are invited for one post of **Junior Research Fellow (JRF)** for the sanctioned project (<u>Project ID: SAP22-NITSrin-045</u>) of Swachhta Action Plan (SAP) scheme 2022 entitled "**An efficient and low-cost hot water supply to rural areas of Kashmir valley using solar PVT technology**". The post is purely temporary and co-terminus with the project. The duration of the project is two (02) years.

| | Details | | | | | | |
|-----------------------------|--|--|--|--|--|--|--|
| Name of the Position | Junior Research Fellow (JRF) | | | | | | |
| No. of Post | 01 (One) | | | | | | |
| Essential Qualifications | B.Tech and M.Tech in Electrical Engineering / Energy Systems or equivalent with minimum 60% aggregate marks (CGPA ≥ 6.5/10) to qualify under Open/OBC category and minimum 55% aggregate marks (CGPA ≥ 6.0) for candidates under SC/ST category. B.Tech (4 years) in Electrical Engineering / Energy Systems or equivalent with minimum 75% aggregate marks (CGPA ≥ 8.0/10) to qualify under Open/OBC category and minimum 70% aggregate marks (CGPA ≥ 7.5) for candidates under SC/ST category, and must have qualified GATE with valid score. | | | | | | |
| Desirable | Candidates having sound knowledge of solar photovoltaic-thermal (PVT) technology and interested to pursue PhD in Electrical Engineering. | | | | | | |
| Age | Upper age limit is 28 years. Age relaxation for reserve candidates will be given as per Govt. norms. | | | | | | |
| Tenure of | The initial appointment will be for one year (12 months) which is extendable up | | | | | | |
| Appointment | to 02 (two) years based on the performance of the candidates. | | | | | | |
| Emoluments | ₹31,000/-per month + HRA @16% | | | | | | |
| How to Apply? | The filled application form (Proforma attached with advertisement) along with CV and scanned copy of the testimonials should be uploaded in the Google Form https://forms.gle/1cSdtmtxvxC4VHd89 SCAN ME | | | | | | |
| Submission | Soft Copy via the above-mentioned Google Form | | | | | | |
| Last Date | 8 th September 2023 | | | | | | |
| Whom to Contact? | Dr. Asadur Rahman Principal Investigator (PI), Department of Electrical Engineering, NIT Srinagar-190006 (J&K) Email: <u>asadur@nitsri.ac.in</u> Contact No. +91 9957497127 (M) | | | | | | |
| Tentative Venue | · · · | | | | | | |
| for Interview | National Institute of Technology Srinagar, Hazratbal – 190006, J&K | | | | | | |

Note:

- 1. The selected candidate may be considered for admission to the PhD program, as per Institute eligibility, rules and statutes.
- 2. The position is purely temporary. The initial appointment will be for one year, which is extendable up to 2 years or the end of the project based on performance.
- 3. At the time of joining, the selected candidate has to produce all relevant documents/testimonials in original for verification and a medical certificate of fitness from the medical officer of the institute. Failing this, the institute will offer the position to the next candidate on the merit list prepared for this purpose. CVs with incomplete information or without supporting documents will be summarily rejected.

Application Procedure: Submit the Google form on or before the deadline. Link & QR Code: https://forms.gle/1cSdtmtxvxC4VHd89



Proforma of the application form is attached to this advertisement.

Note:

- CV should include contact details (address, mobile phone no., email ID), date of birth, qualifications, research experience, and publication details (if any).
- All the testimonials (scanned copies of mark sheets, degree certificates, experience certificates, GATE certificates, valid identity proof, etc.) in pdf as a single compressed (zip/RAR format) file must be uploaded in the above-mentioned Google form.

Deadline for application: 8th September 2023.

Screening & Interview Process

Candidates will be shortlisted based on their merit and as per the requirements of the project. Mere qualification of eligibility criteria does not guarantee shortlisting. The interview shall be tentatively conducted in the month of September 2023. The date, time, and venue of the interview will be intimated to short-listed candidates via email. No TA/DA will be paid for attending the interview. Candidates who are already employed should produce a 'No Objection Certificate' (NOC) from the present employer.

For any further clarifications, candidates may contact:

Dr. Asadur Rahman Principal Investigator (PI), Department of Electrical Engineering, NIT Srinagar-190006 (J&K) Email: <u>asadur@nitsri.ac.in</u> Contact No. +91 9957497127 (M)

विद्युत अभियांत्रिकी विभाग / DEPARTMENT OF ELECTRICAL ENGINEERING राष्ट्रीय प्रौद्योगिकी संस्थान श्रीनगर / NATIONAL INSTITUTE OF TECHNOLOGY SRINAGAR

(शिक्षा मंत्रालय, भारत सरकार के तत्वावधान में राष्ट्रीय महत्व का एक संस्थान) (An Institute of National Importance under the aegis of Ministry of Education, Govt. of India) हजरतबल, श्रीनगर, जम्मू-कश्मीर, १९०००६, भारत Hazratbal, Srinagar Jammu and Kashmir, 190006, INDIA



Application Form for Junior Research Fellow (JRF)

Project ID: SAP22-NITSrin-045 of Swachhta Action Plan (SAP) scheme 2022

6. Category (SC/ST/PwD/OBC/Open):.....

Recent Passport size Photograph

7. Academic Records:

(a) Qualifying Degree Details:

| Degree | University / Board | Grade/ Division | CGPA/ %age | Year of Passing | Branch/ Specialisation/ Main Subject(s) | Remarks |
|--------------------|--------------------|--------------------|---------------|--------------------|---|---------|
| M.Tech | | | | | | |
| B.Tech | | | | | | |
| 12 th / | | | | | | |
| Intermediate | | | | | | |
| 10 th | | | | | | |

(b) GATE Score:

| (3) 4111 2 8 8 8 1 8 | | | | |
|----------------------|------|--------|----------------|----------------|
| Exam | Year | Scores | All India Rank | Validity Up to |
| GATE | | | | |

- 8. Whether interested for Ph.D. in Electrical Engineering? (Yes or No):
- 9. Publications/Conference participations (use additional sheet, if required):
- 10. Corresponding Address:

.....

- Mobile No. :.....E-mail ID:....
- 11. Any other relevant information:
- 12. **Declaration:** I hereby declare that the information provided herewith is correct to the best of my knowledge and belief and nothing has been concealed. If any information at any stage is found to be incorrect then I shall be liable for action taken by the authority as per existing rules/law.

Date:

(Signature of Candidate)