

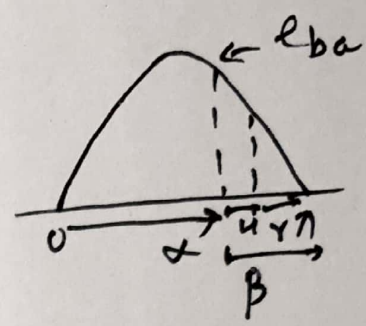
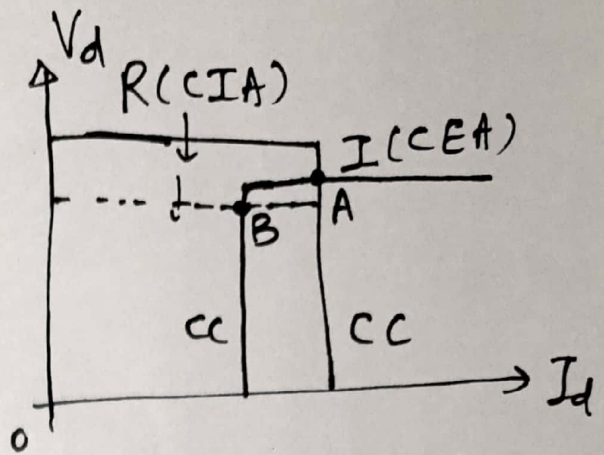
Control characteristics:

V - control (Inverter)



$V = V_{min.} \text{ (constant)}$

$V_{ref.} \quad V_{actual}$   
 $15^\circ \quad \downarrow$   
 $10^\circ \Rightarrow 15^\circ$



- V - control is also not 100% ideal.

- There is a substantial amount of risk of commutation failure.  $\beta = \gamma + \mu$

$I_d = I_{s3} (C_{\gamma} \gamma - C_{\beta} \beta)$

$I_d = \frac{\sqrt{3} E_m}{2\omega L} (C_{\gamma} \gamma - C_{\beta} \beta)$

Let inverter side AC voltage fall.

$I_d \uparrow$

$I_d = \frac{\sqrt{3} E_m}{2\omega L} (C_{\gamma} \gamma - C_{\beta} \beta)$

$V$  is fixed at  $V_{ref.}$

$\therefore$  For given  $V, I_d, E_m, \omega$  &  $L, \beta$  is calculated.

(Q) The above computation ( $\beta$ -calculation) is based on reduction of AC-side voltage of inverter due to 3- $\phi$  symmetrical fault.

For asymmetrical faults,  $u$  increases and  $\beta$  is wrongly computed by the controller; hence resulting in commutation failure. (2)

ii) If rate of rise of  $I_d$  is small, it will be taken care of by controller. But if  $dI_d/dt$  is fast, it will result in wrong calculation of  $\beta$  and hence commutation failure.

iii) Commutation failure may occur when voltage reduction on inverter AC side takes place just after the firing of a valve.

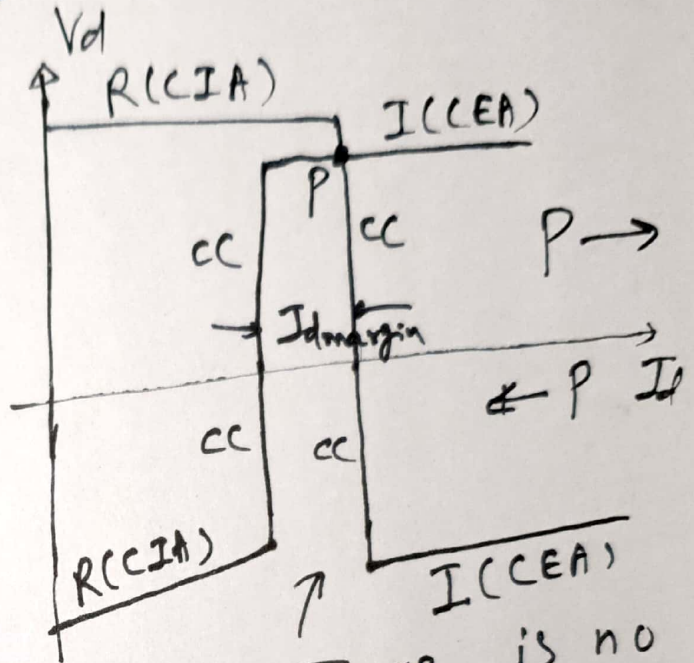
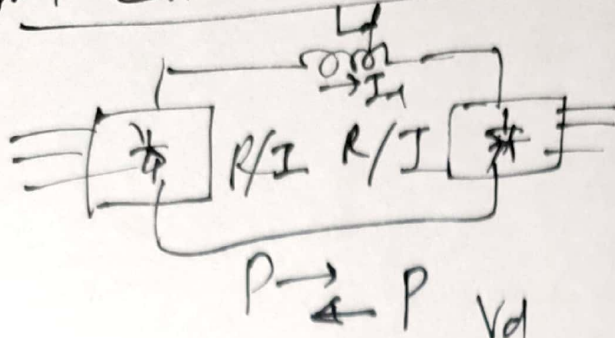
But the probability of this is low as compared to (i) & (ii) above. Therefore, it is not sufficient to compute  $E_m$  and  $I_d$  only; following is to be considered:

- a) Amplitude of commutation voltage
- b) Phase of " "
- c) Magnitude of  $I_d$
- d) Rate of change of DC line current ( $dI_d/dt$ )

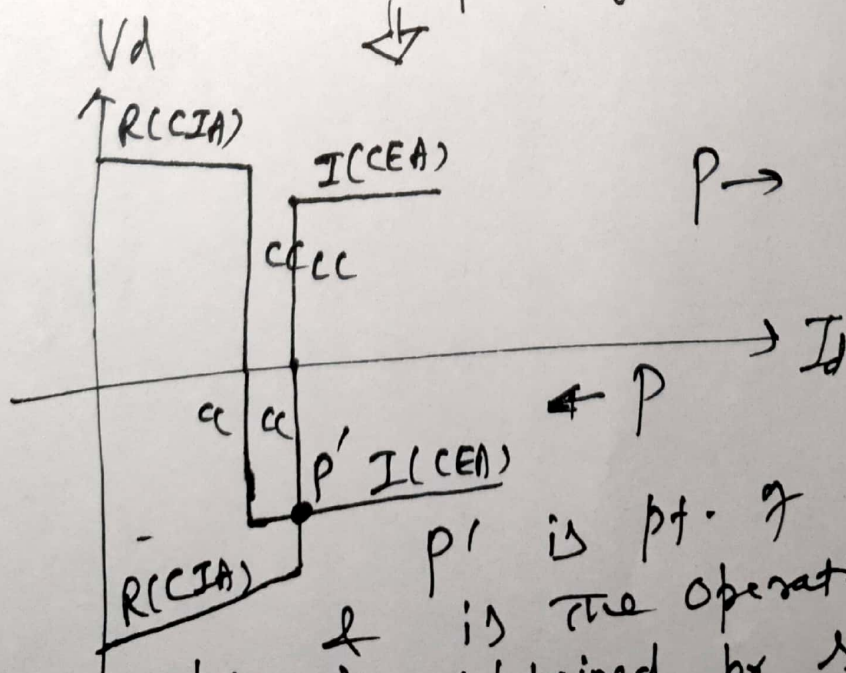
$$\gamma = \underline{\underline{V_{ref}}}$$



Control characteristics for Power-Reversal:



Since there is no pt. of intersection, no operating pt.



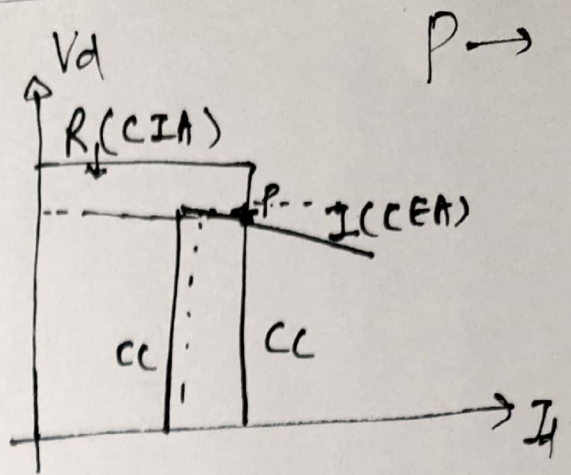
This opern. pt. is obtained by swapping the cc characts. of  $R$  &  $I$  for reverse power flow.

Corrections in Control Characteristics:

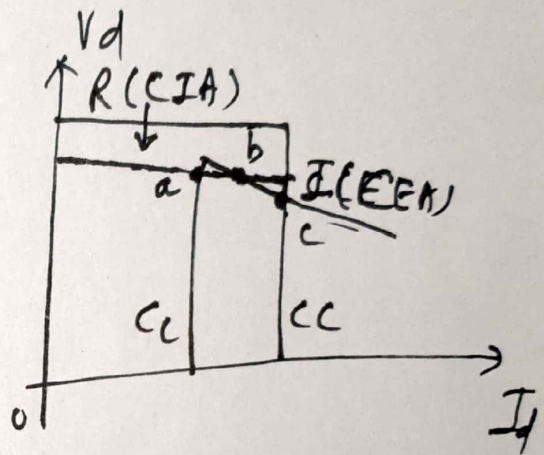
Due to fault, the inverter-side AC voltage reduces.

$$I_d \uparrow \rightarrow U \uparrow$$

$$\rightarrow R_{ci} \uparrow$$



$$\underline{V_{di}} = V_{doi} \cos \gamma - \underline{R_{ci} I_d}$$



Due to drooping nature of inverter charact., there are

now three pts. of intersection bet.  $R$  &  $I$  characts. (a, b & c).

$\Rightarrow$  Controller hunts for stable operating point & it will keep on hunting.

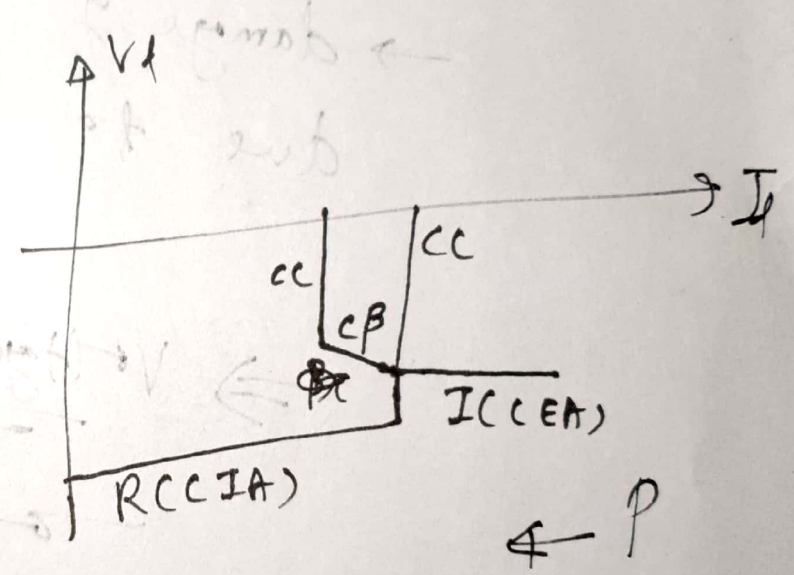
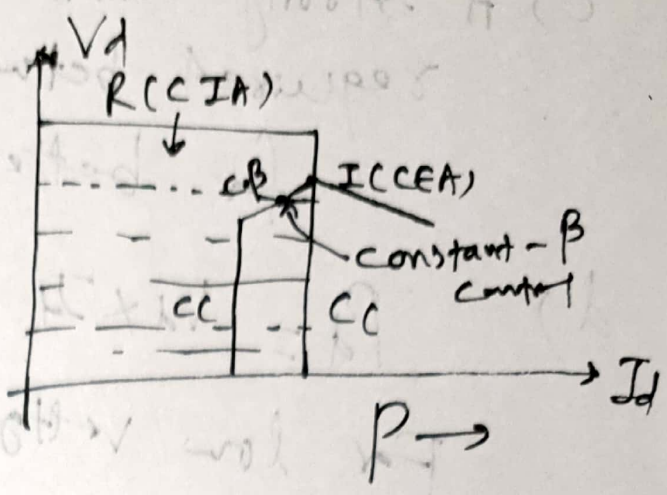
$\Rightarrow$  Controller of HVDC Link  $\rightarrow$  unstable.

$\Rightarrow$  To address this issue, inverter charact. is modified either with constant voltage control or constant- $\beta$  control.

$\downarrow$   
Worsens pf



Modified control characts:



Other considerations:

- Always voltage or current controllers are used instead of DC power controls

because:

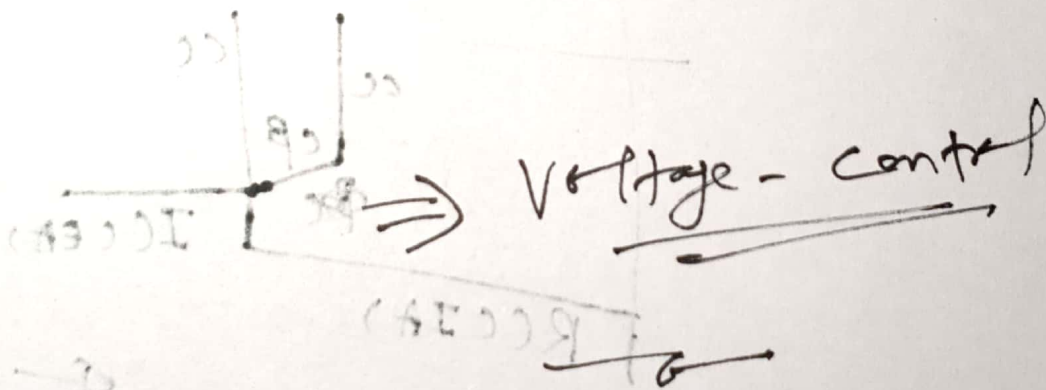
a) Line resistance,  $R_l \downarrow \rightarrow$  Smaller damping factor  
 $\rightarrow$  Huge power oscillations  $\rightarrow$  unstable oper.

b)  $P_d = V_d \cdot I_d \rightarrow$  controller becomes complex & loses flexibility

c) A strong communication-link is required between both sides for better control.

d)  $P_d = V_d \times I_d$

For low voltage,  $P_d = I_d V_d + I_d^2 R$   
 → damage of valves (converters) due to high  $I_d$ .

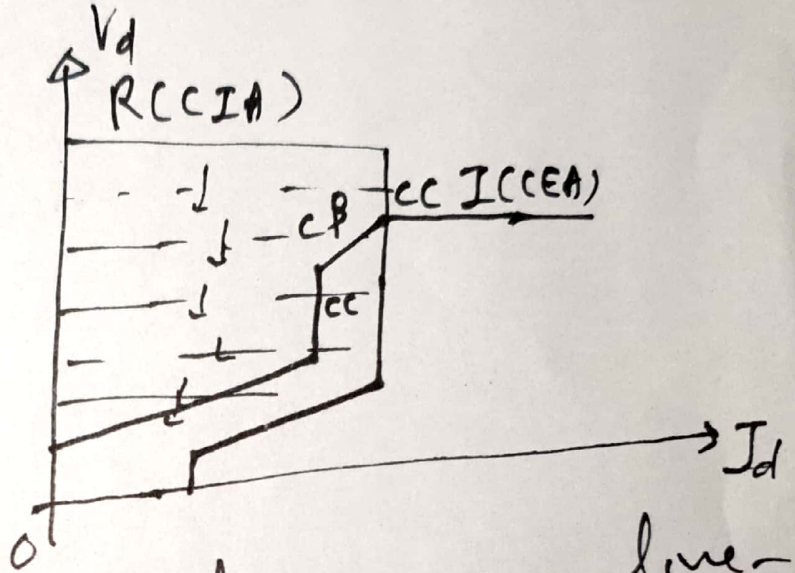


# Incorporating VDCOL in control

(6)

Characteristics:

VDCOL: Voltage-Dependent current-order Limit



Controller puts some limit on live-current to avoid damage.

⇒ VDCOL

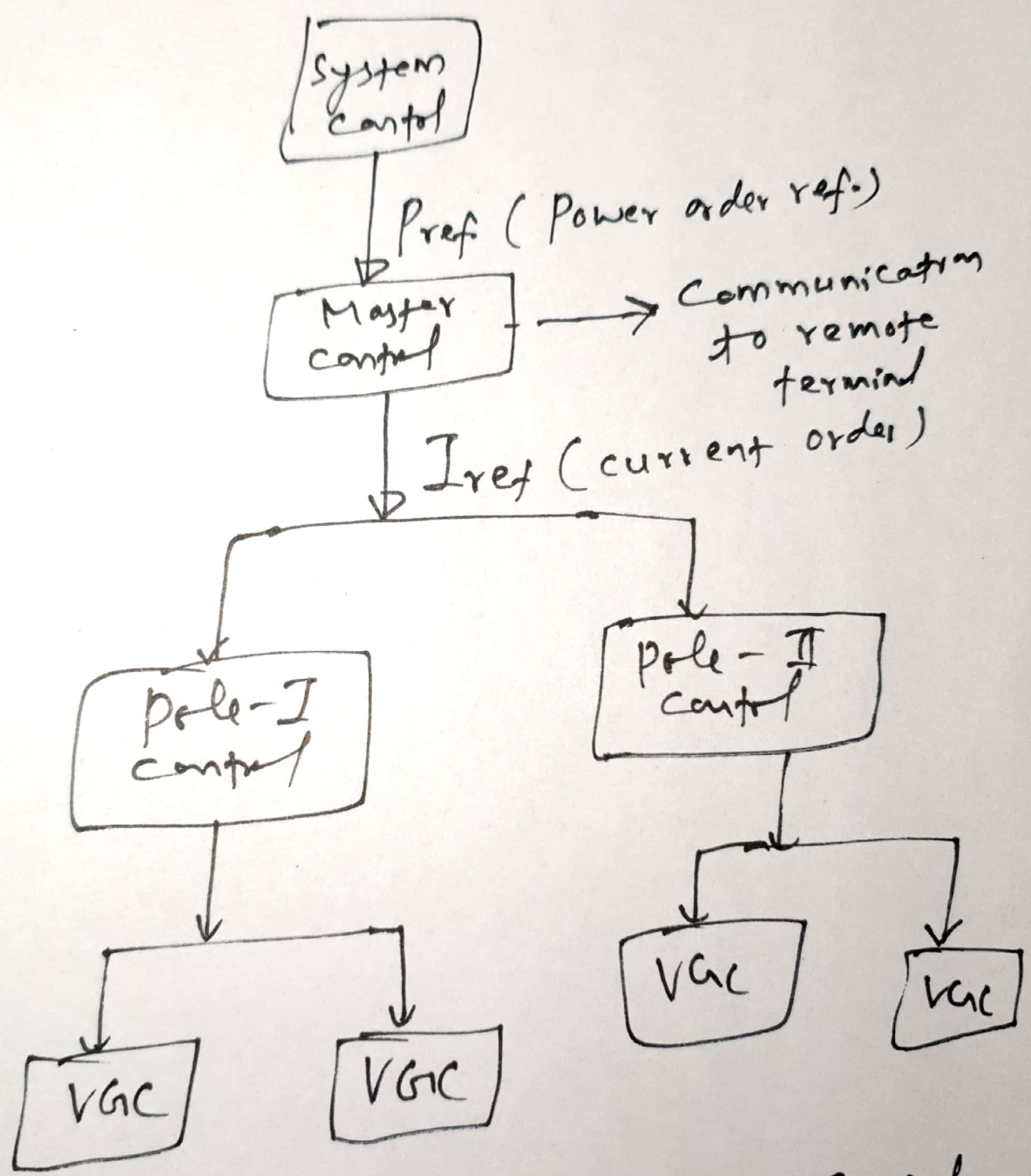
Final control characteristics:

R: CIA, CC, VDCOL

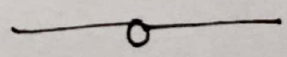
I: CEA, Cβ, CC, VDCOL



# Controller Design Philosophy (Hierarchy):

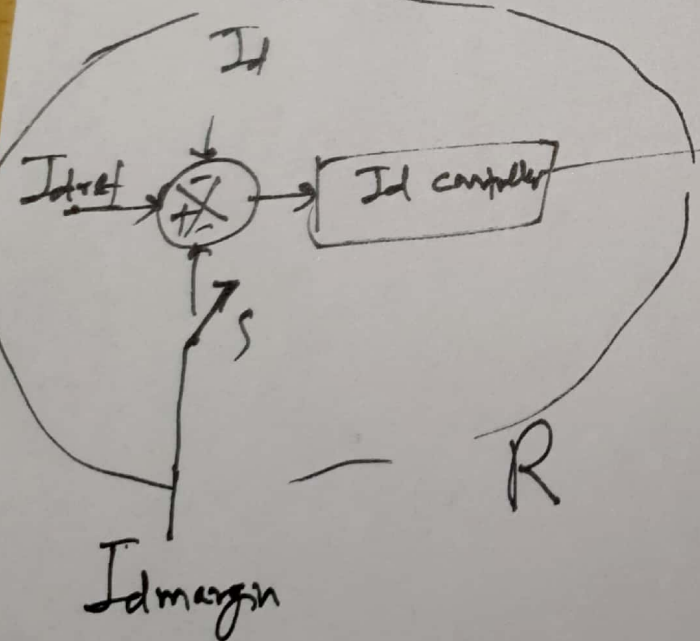
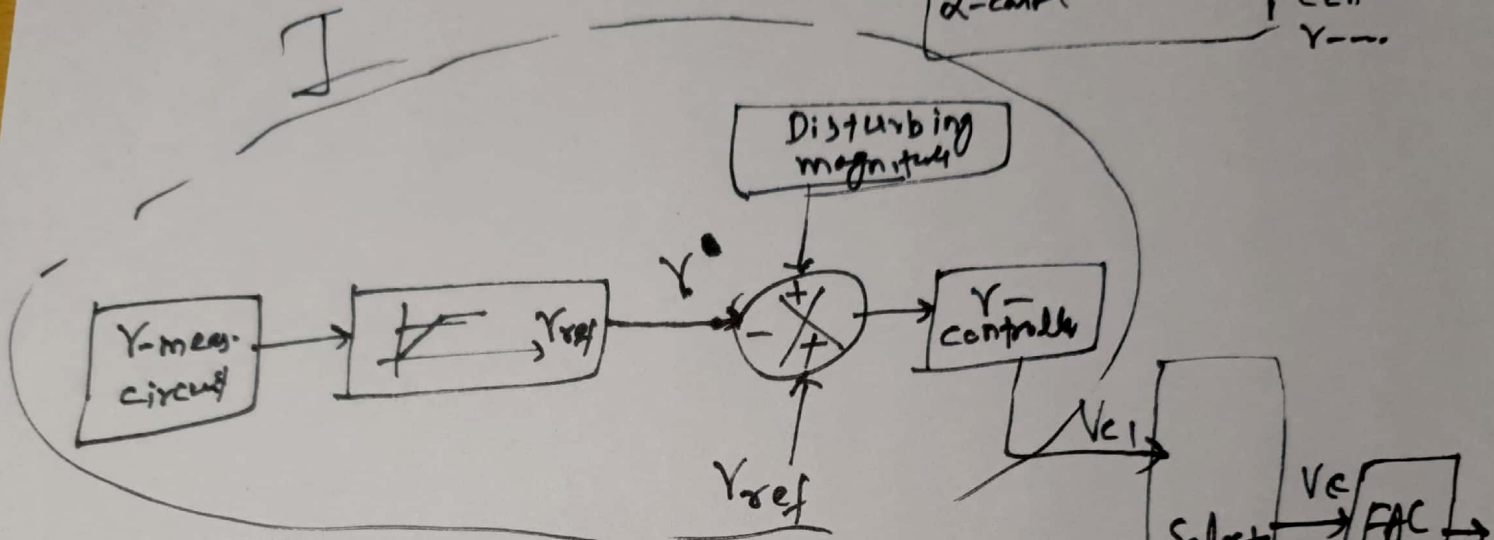
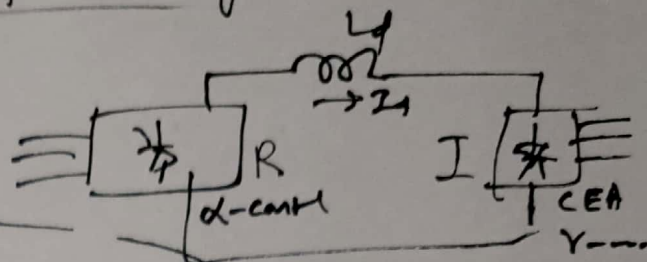


VGC: Valve Group Control





# Controller schematic diagram:



FAC: Firing Angle ctrl

